

Advertisement

Applications are invited for one Research Associate, two Junior Research Fellows and one Technical Assistant at the Department of Electronic Science, Savitribai Phule Pune University, to work on project "SEAMS" (Space Electric and Magnetic Sensor) for very low frequency radio astronomy(0-16 MHz)

Number and Name of the post:	One, Research Associate (RA)
Category of post:	Open to all
Fellowship:	Rs. 36,000
Number and Name of the post:	Two, Junior Research fellow(JRF)
Category of post:	one Open, one SC
Fellowship:	Rs. 25,000
Number and Name of the post:	One Technical assistant (TA)
Category of post:	Open to all
Fellowship:	Rs.15,000

Duration of the project: one year

The project is aimed at the designing a payload to be deployed on the PSLV fourth stage (PS4 platform). The research associate has to coordinate all the activities and work on the integration and realization of the engineering model of the system. One JRF is expected to work on Antenna design and deployment, and design of LNA's, Filters, Pre amplifiers etc and other on FPGA based system, Data acquisition, FFT implementation etc. The TA is supposed to work on the mechanical design of the antenna, the deployment mechanism and well as help in making engineering drawings, soldering, and testing the system.

Eligibility and Necessary Qualification for

Research Associate: Doctorate or equivalent degree in Science/Engineering/ Technology or having 3 years of research, design and development experience, after MSc (Electronic Science/Physics) / MTech with at least one research paper in Science Citation Indexed (SCI) journal and experience in RF/ FPGA based system design, data acquisition and spectrum analysis.

JRF: M.Sc. in Electronic Science /Physics / BE E&TC with experience or M. Tech. Good academic record and NET/SET/Gate qualified candidates will be given preference. Research/work experience in the field of Antenna, RF design/ FPGA based system design is desirable.

TA: M.Sc Electronic Science/Physics /BE Mechanical / E&TC. Familiarity with CAD tools is desirable.

Plain-paper application, including a covering letter and a resume/CV, addressed to Dr. Mrs. D. C. Gharpure, Department of Electronic Science, Savitribai Phule Pune University, Pune 411007, should be submitted in the Office on or before 10/5/2019 or sent via email to tdcg@electronics.unipune.ac.in. Resume should include: full name, contact information (address, email, and contact number), date of birth, educational qualifications and relevant experience.

The interviews will be held on 17th May at 11.30 am in the Department of Electronic Science.



Head
Department of Electronic Science,
SP Pune University



Registrar
Savitribai Phule Pune University