

**Savitribai Phule Pune University
(Formerly University of Pune)**



**Board of Studies, Department of Technology
Electronics & Electrical (EE) Technology
Curriculum Structure for M.Tech Program**

Sr. No.	Subject Code	Subject Name	Credits	Teaching Scheme (Theory)	Teaching Scheme (Lab)
Semester (I)					
1	MMaths	Mathematics for Technology	3	√	
2	EEC2	Core Subject 2	3	√	
3	EEC3	Core Subject 3	3	√	
4	EEC4	Core Subject 4	3	√	
5	EEE*	Elective-1	3	√	
6	EELP1	Lab Practice - 1	3		√
7	EESM1	Seminar - 1	1		√
Semester (II)					
8	EEC5	Core Subject 5	3	√	
9	EEC6	Core Subject 6	3	√	
10	EEC7	Core Subject 7	3	√	
11	EEC8	Core Subject 8	3	√	
12	EEE*	Elective-2	3	√	
13	EELP2	Lab Practice - 2	3		√
14	EESM2	Seminar - 2	1		√
Semester (III)					
15	TSD	Technical Skill Development	3	√	
16	EEE*/EEDS*	Elective 3 /DS(Directed Study)	3	√	
17	EEIntProj	Interim Project	10		√
Semester (IV)					
18	EEFinProj	Final Project (Dissertation Submission)	16		√
		TOTAL CREDITS	70		