

S.E.(Information Technology)
PROCESSOR ARCHITECTURE
(2019Pattern) (Semester-II)

1. Explain in brief Interrupt vs Polling.
2. Describe steps for executing Interrupt.
3. State and explain external hardware Interrupt.
4. Write short note enabling and disabling Interrupt.
5. Differentiate between ARM and RISC.
6. Explain the PWM generation.
7. Write short note on stepper motor interfacing with PIC.
8. Write short note on SPI.
9. Explain the interfacing of ADC and DAC 0808 with PIC.
10. Write short note on interfacing of RTC using I2C with PIC.
11. Explain CPSR and SPSR register.
12. Explain features and Advantages of ARM processors.