SAVITRIBAI PHULE PUNE UNIVERSITY
(Formerly University of Pune)

EXAMINATION CIRCULAR NO.172 OF 2019
PROGRAMME OF M.SC. ELECTRONICS SCIENCE SEM. I TO IV CREDIT SYSTEM (2008 PATTERN)

Examination of APR/MAY-2019
(Under Faculty of SCIENCE AND TECHNOLOGY : A)SCIENCE)

INSTRUCTIONS FOR CANDIDATES

• Candidates are required to be present at the examination centre, THIRTY MINUTES before the time fixed for paper.
• Candidates are forbidden from taking any material into the examination hall, that can be used for malpractice at the time of examination.
• **Candidates are requested to see the Notice Board at their place of examination regularly for changes if any, that may be notified later in the program.**
• No request for any special concession such as a change in time or any day fixed for the University Examination on any ground shall be granted.
• Use of non programmable calculator is permitted.

• The Examination of candidates of University Post-Graduation Dept. having credit system will be held at Their Respective Department
### PROGRAMME OF M.SC. ELECTRONICS SCIENCE
#### SEM. I TO IV CREDIT SYSTEM (2008 PATTERN)/172/F-2019

<table>
<thead>
<tr>
<th>Day &amp; Date</th>
<th>SEMISTER-I Time-10.00 AM To 01.00 PM</th>
<th>SEMISTER-III Time-02.00 PM To 05.00 PM</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thursday 02/05/2019</td>
<td>EL1 UT01 FOUNDATION OF SEMICONDUCTOR DEVICES</td>
<td>EL3UT05 EMBEDDED SYSTEMS</td>
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<tr>
<td>Friday 03/05/2019</td>
<td>EL1 UT02 ANALOG CIRCUIT DESIGN &amp; ANALYSIS</td>
<td></td>
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<tr>
<td>Saturday 04/05/2019</td>
<td>EL1 UT03 INSTRUMENTATION &amp; MEASUREMENT TECHNIQUES</td>
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<thead>
<tr>
<th>Day &amp; Date</th>
<th>SEMISTER-II Time-10.00 AM To 01.00 PM</th>
<th>SEMISTER-IV Time-02.00 PM To 05.00 PM</th>
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<tbody>
<tr>
<td>Thursday 09/05/2019</td>
<td>EL2 UT04 APPLIED ELECTROMAGNETICS, RF &amp; MICROWAVE</td>
<td>EL4UT-06 CONTROL SYSTEM: THEORY &amp; APPLICATION</td>
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<tr>
<td>Friday 10/05/2019</td>
<td>EL2 UT05 COMMUNICATION ELECTRONICS</td>
<td></td>
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<tr>
<td>Saturday 11/05/2019</td>
<td>EL2 UT06 DIGITAL SYSTEM DESIGN USING VHDL</td>
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**NOTE:**

1) The Practical Examination will be conducted before theory examination between 10/04/2019 to 23/04/2019. Due notice for the place, date and time of the practical examination will be displayed on the Notice Board. Candidates are directed to be present at time and place of the practical examination.

2) Last Examination at 2008 pattern will be held in May 2019. Candidate will be allowed to appear as per 2008 pattern till May 2019 or his/her valid term which ever is early.

Ganeshkhind, Pune - 411 007

Ref.No/XCT/1106

Date:09/04/2019

Dr. Ashok Chavan

Director

Board of Examinations And Evaluation